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Dr. Konstadinidis holds 13 patents and has several IEEE publications. He served as a member of the ISSCC Digital Program Committee from 2002 to 2007, and as Guest Editor for the IEEE Journal of Solid State Circuits. He is a co-author of the book "Clocking in Modern VLSI Systems", Springer, 2009. He currently serving as TPC member of the IRPS Digital Circuit Reliability sub-committee and IEDM Systems & Circuit Reliability